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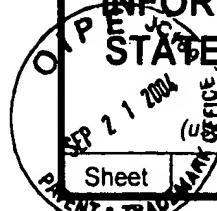
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Application Number	09/620,544
Filing Date	07/21/2000
First Named Inventor	Petro Estakhri
Group Art Unit	2188
Examiner Name	Reginald G. Bragdon
Attorney Docket Number	38979-11C5(New Docket No.)



**U.S. PATENT DOCUMENTS**

		U.S. Patent Document		Date of Publication of	Pages, Columns, Lines,
RGB	A1	2003/0033471	A1	Lin et al.	02/13/2003
	A2	6,411,546	B1	Estakhri et al.	06/05/2002
	A3	6,397,314	B1	Estakhri et al.	05/28/2002
	A4	6,393,513	B2	Estakhri et al.	05/21/2002
	A5	6,374,337	B1	Estakhri	04/16/2002
	A6	6,279,114	B1	Toombs et al.	08/21/2001
	A7	6,279,069	B1	Robinson et al.	08/21/2001
	A8	6,272,610	B1	Katayama et al.	08/07/2001
	A9	6,262,918	B1	Estakhri et al.	07/17/2001
	A10	6,230,234	B1	Estakhri et al.	05/08/2001
	A11	6,226,708	B1	McGoldrick et al.	05/01/2001
	A12	6,223,308	B1	Estakhri et al.	04/24/2001
	A13	6,202,138	B1	Estakhri et al.	03/13/2000
	A14	6,182,162	B1	Estakhri et al.	01/30/2001
	A15	6,181,118	B1	Meehan et al.	01/30/2001
	A16	6,173,362	B1	Yoda	01/09/2001
	A17	6,151,247		Estakhri et al.	11/21/2000
	A18	6,145,051		Estakhri et al.	11/07/2000
	A19	6,141,249		Estakhri et al.	10/31/2000
	A20	6,134,151		Estakhri et al.	10/17/2000
	A21	6,134,145		Wong	10/17/2000
	A22	6,128,695		Estakhri et al.	10/03/2000
	A23	6,125,435		Estakhri et al.	09/26/2000
	A24	6,125,424		Komatsu et al.	09/26/2000
	A25	6,122,195		Estakhri et al.	09/19/2000
	A26	6,115,785		Estakhri et al.	09/05/2000
	A27	6,097,666		Sakui et al.	08/01/2000
	A28	6,081,878		Estakhri et al.	06/27/2000
	A29	6,081,447		Lofgren et al.	06/27/2000
	A30	6,072,796		Christensen et al.	06/06/2000
	A31	6,055,188		Takeuchi et al.	04/25/2000
	A32	6,055,184		Acharya et al.	04/25/2000
	A33	6,047,352		Lakhani et al.	04/04/2000
	A34	6,040,997		Estakhri	03/21/2000
	A35	6,035,357		Sakaki	03/07/2000
	A36	6,034,897		Estakhri et al.	03/07/2000
	A37	6,026,027		Terrell, II et al.	02/15/2000
	A38	6,026,020		Matsubara et al.	02/15/2000
	A39	6,021,408		Ledain et al.	02/01/2000
	A40	6,011,323		Camp	01/04/2000
	A41	6,011,322		Hiraka	01/04/2000
	A42	5,991,849		Yamada et al.	11/23/1999
RGB	A43	5,987,573		Hiraka	11/16/1999

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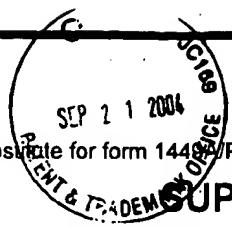
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6

<i>RGB</i>	A44	5,987,563	Itoh et al.	11/16/1999
	A45	5,986,933	Takeuchi et al.	11/16/1999
<i>RGB</i>	A46	5,966,727	Nishino et al.	10/12/1999
	A47	5,959,926	Jones et al.	09/28/1999
	A48	5,956,473	Ma et al.	09/21/1999
	A49	5,953,737	Estakhri et al.	09/14/1999
	A50	5,937,425	Ban	08/10/1999
	A51	5,936,971	Harari et al.	08/10/1999
	A52	5,933,846	Endo	08/03/1999
	A53	5,933,368	Ma et al.	08/03/1999
	A54	5,930,815	Estakhri et al.	07/27/1999
	A55	5,928,370	Asnaashari	07/27/1999
	A56	5,920,884	Jennings, III et al.	07/06/1999
	A57	5,909,586	Anderson	06/01/1999
	A58	5,907,856	Estakhri et al.	05/25/1999
<i>RGB</i>	A59	5,901,086	Wang et al.	05/04/1999
	A60	5,890,192	Lee et al.	03/30/1999
<i>RGB</i>	A61	5,862,099	Gannage et al.	01/19/1999
	A62	5,860,124	Matthews et al.	01/12/1999
	A63	5,860,083	Sukeawa	01/12/1999
	A64	5,847,552	Brown	12/08/1998
	A65	5,845,313	Estakhri et al.	12/01/1998
	A66	5,835,935	Estakhri et al.	11/10/1998
	A67	5,831,929	Manning	11/03/1998
	A68	5,822,781	Wells et al.	10/13/1998
	A69	5,822,252	Lee et al.	10/13/1998
	A70	5,822,245	Gupta et al.	01/13/1998
	A71	5,818,781	Estakhri et al.	10/06/1998
	A72	5,809,560	Schneider	09/15/1998
	A73	5,809,558	Matthews et al.	09/15/1998
	A74	5,809,515	Kaki et al.	09/15/1998
	A75	5,802,551	Komatsu et al.	09/01/1998
	A76	5,799,168	Ban	08/25/1998
	A77	5,787,484	Norman	07/28/1998
	A78	5,787,445	Daberkow	07/28/1998
	A79	5,781,478	Takeuchi et al.	07/14/1998
	A80	5,773,901	Kanter	06/30/1998
	A81	5,768,195	Nakamura et al.	06/16/1998
	A82	5,768,190	Tanaka et al.	06/16/1998
	A83	5,761,117	Uchino et al.	06/02/1998
	A84	5,758,100	Odisho	05/26/1998
	A85	5,757,712	Nagel et al.	05/26/1998
	A86	5,754,567	Norman	05/19/1998
	A87	5,745,418	Ma et al.	04/28/1998
	A88	5,734,567	Griffiths et al.	03/31/1998
<i>RGB</i>	A89	5,723,990	Roohparvar	03/03/1998



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Sheet	3	of	6	Attorney Docket Number	Examiner Name	Reginald G. Bragdon
RGB	A90	5,648,929			Miyamoto	07/15/1997
	A91	5,611,067			Okamoto et al.	03/11/1997
	A92	5,603,001			Sukegawa et al.	02/11/1997
	A93	5,602,987			Harari et al.	02/11/1997
	A94	5,598,370			Niisima et al.	01/28/1997
	A95	5,592,415			Kato et al.	01/07/1997
	A96	5,581,723			Hasbun et al.	12/03/1996
	A97	5,579,502			Konishi et al.	11/26/1996
	A98	5,572,466			Sukegawa	11/05/1996
	A99	5,552,698			Tai et al.	09/03/1996
	A100	5,544,356			Robinson	08/06/1996
	A101	5,541,551			Brehner et al.	07/30/1996
	A102	5,530,938			Akasaka et al.	06/25/1996
	A103	5,530,828			Kaki et al.	06/25/1996
	A104	5,530,673			Tobila et al.	06/25/1996
	A105	5,524,230			Sakaue et al.	06/04/1996
	A106	5,523,980			Sakui et al.	06/04/1996
	A107	5,519,847			Fandrich et al.	05/21/1996
	A108	5,515,333			Fujita et al.	05/07/1996
	A109	5,513,138			Manabe et al.	04/30/1996
	A110	5,490,117			Oda et al.	02/06/1996
	A111	5,479,638			Assar et al.	12/26/1995
	A112	5,473,765			Gibbons et al.	12/05/1995
	A113	5,465,338			Clay	11/07/1995
	A114	5,465,235			Miyamoto	11/07/1995
	A115	5,431,330			Wieres	07/11/1995
	A116	5,430,682			Ishikawa et al.	07/04/1995
	A117	5,422,856			Sasaki et al.	06/06/1995
	A118	5,418,752			Harari et al.	05/23/1995
	A119	5,406,527			Honma	04/11/1995
	A120	5,404,485			Ban	04/04/1995
	A121	5,388,083			Assar et al.	02/07/1995
	A122	5,384,743			Rouy	01/24/1995
	A123	5,382,839			Shinohara	01/17/1995
	A124	5,381,539			Yanai et al.	01/10/1995
	A125	5,371,702			Nakai et al.	12/06/1994
	A126	5,365,127			Manley	11/15/1994
	A126B	5,359,569			Fujita et al.	10/25/1994
	A127	5,341,341			Fukazo	08/23/1994
	A128	5,341,339			Wells	08/23/1994
	A129	5,341,330			Wells et al.	08/23/1994
RGB	A130	5,329,491			Brown et al.	07/12/1994



**SUPPLEMENTAL  
INFORMATION DISCLOSURE  
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of

6

RGB	A131	5,315,558	Hag	05/24/1994
	A132	5,305,278	Inoue	04/19/1994
	A133	5,305,276	Uenoyama	04/19/1994
	A134	5,303,198	Adachi et al.	04/12/1994
	A135	5,267,218	Elbert	11/30/1993
	A136	5,253,351	Yamamoto et al.	10/12/1993
	A137	5,227,714	Lou	07/13/1993
	A138	5,220,518	Haq	06/15/1993
	A139	5,218,695	Noveck et al.	06/08/1993
	A140	5,093,785	Iijima	03/03/1992
	A140A	4,970,727	Miyawaki et al.	11/13/1990
	A140B	4,970,642	Yamamura	11/13/1990
	A141	4,943,745	Watanabe et al.	07/24/1990
	A142	4,843,224	Ohta et al.	06/27/1989
	A143	4,829,169	Watanabe	05/09/1989
	A144	4,797,543	Watanabe	01/10/1989
	A145	4,788,665	Fukuda et al.	11/29/1988
	A146	4,780,855	Iida et al.	10/25/1988
	A147	4,609,833	Guterman	09/02/1986
	A148	4,532,590	Wallach et al.	07/30/1985
	A149	4,476,526	Dodd	10/09/1984
	A150	4,473,878	Zolnowsky et al.	09/25/1984
	A151	4,468,730	Dodd et al.	08/28/1984
	A152	4,414,627	Nakamura	11/08/1983
	A153	4,398,248	Hsia et al.	08/09/1983
	A154	4,309,627	Tabata	01/05/1982
	A155	4,130,900	Watanabe	12/19/1978
	A156	4,099,069	Cricchi et al.	07/04/1978
RGB	A157	35,881 Re.	Barrett et al.	08/25/1998 (Reissued)

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No.*	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Count	Number <sup>4</sup>				
B1			0 663 636	A1 Sun Microsystems, Inc.	07/19/1995		
B2			0 619 541	A2 Hitachi, Ltd., Hitachi Keiryo Engineering Co., Ltd., Hitachi ULSI Engineering Corp.	10/12/1994		
B3			0 617 363	A2 Sundisk Corporation	09/28/1994		
B4			0 613 151	A2 Kabushiki Kaisha Toshiba	08/31/1994		
B5			0 522 780	A2 International Business Machines Corporation	01/13/1993		
B6			0 522 780	B1 International Business Machines Corporation	01/13/1993		
B7			0 392 895	A2 Sundisk Corporation	10/17/1990		

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Application Number	09/620,544
Filing Date	07/21/2000
First Named Inventor	Petro Estakhri
Group Art Unit	2188
Examiner Name	Reginald G. Bragdon
Attorney Docket Number	38979-11C5(New Docket No.)

B8	2 304 428		Mitsubishi Denki Kabushiki Kaisha	03/19/1997	
B9	2 291 990		Memory Corporation	02/07/1996	
B10	2 251 323		Intel Corporation	07/01/1992	
B11	9-147581		Hitachi Co., Ltd., Hitachi VLSI Engineering Corp.	06/06/1997	
B12	8-69696		Toshiba Corp.	03/12/1996	
B13	8-18018		Toshiba Corp.	01/19/1996	
B14	7-93499		Hitachi Co., Ltd., Hitachi VLSI Engineering Corp., Hitachi Tobu Semiconductor Co., Ltd.	04/07/1995	
B15	7-311708		Fuji Film Micro Device K.K., Fuji Photo Film Co., Ltd.	11/28/1995	
B16	7-114499			05/02/1995	
B17	6-36578		Sony Kabushiki Kaisha	02/10/1994	
B18	6-266596		Hitachi Ltd.	09/22/1994	
B19	6-149395		NEC	05/27/1994	
B20	6-132747		Fujitsu Ltd., Fujitsu VLSI Ltd.	05/13/1994	
B21	6-131889		Toshiba Corporation	05/13/1994	
B22	6-124231		Toshiba Corporation	05/06/1994	
B23	6-124175		Sharp Corp.	05/06/1994	
B24	5-282883		Toshiba Corp.	10/29/1993	
B25	5-128877		Mitsubishi Electric Corp.	05/25/1993	
B26	4-57295		NEC Corporation	02/25/1992	
B27	4-278297		Toshiba Corp.	10/02/1992	
B28	4-268284		Fuji Photo Film Co., Ltd	09/24/1992	
B29	4-254994		Toshiba Corp.	09/10/1992	
B30	RM-37697			02/21/1996	
B31	3-228377		Toshiba Corp.	10/09/1981	
B32	1-138694		Nippon Denki Kabushiki Kaisha	05/31/1989	
B33	WO 94/20906		M-Systems Ltd.	09/15/1994	
B34	SU 1686449	A2	N.G. Parkhomenko, S.B. Kozelkov, V.Yu Lozbenev and S.S. Karpenko	10/23/1991	
B35	SU 1573458	A2	N.G. Parkhomenko, V. Yu. Lozbenev, V.G. Chernyaev and S.B. Kozelkov	06/23/1990	
B36	SU 1541619	A1	K.G. Semenov, N.M. Sidorov, A.I. Zhdanov, G.V. Kukhar and V.I. Potapenko	02/07/1990	
B37	SU 1515164	A1	I.V. Dementiev and A.S. Papkov	10/15/1989	
B38	SU 1408439	A1	V.V. Merkul, I.Yu. Manukin and M.N. Gurevich	07/07/1988	
B39	SU 1388877	A1	N.G. Parkhomenko, V. Yu. Lozbenev and A.P. Kuprovskii	04/15/1988	

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Attorney Docket Number	38979-11C5(New Docket No.)

**OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	<table border="0"> <tr><td>C1</td><td>Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.</td></tr> <tr><td colspan="2">MENDEL ROSENBLUM and JOHN K. CUSTERHOUT, The Design and Implementation of a Log-Structured File System, article, 1991, 15 pgs., Berkeley, USA</td></tr> <tr><td>C2</td><td>BRIAN DIPERT and MARKUS LEVY, Designing with Flash Memory, book, April 1994, 445 pgs., Annabooks, San Diego, USA</td></tr> <tr><td>C4</td><td>ROSS S. FINLAYSON and DAVID R. CHERITON, An Extended File Service Exploiting Write-Once Storage, article, 1987, 10 pgs. ACM</td></tr> <tr><td>C5</td><td>JASON GAIT, The Optical File Cabinet: A Random-Access File System for Write-Once Optical Disks, article, June 1988, 12 pgs., Beaverton, Oregon</td></tr> <tr><td>C6</td><td>HENRY G. BAKER, Memory Management, book, 1995, 19 pgs., Springer-Verlag Berlin Heidelberg, Germany</td></tr> <tr><td>C7</td><td>SAPE J. MULLENDER and ANDREW S. TANENBAUM, A Distributed File Service Based on Optimistic Concurrency Control, article, 1985, 12 pgs., ACM</td></tr> <tr><td>C8</td><td>HIROSHI NAKAMURA, JUNICHI MIYAMOTO, KENICHI IMAMIYA and YOSHIHISA IWATA, A Novel Sense Amplifier for Flexible Voltage Operation NAND Flash Memories, symposium, 1995, VLSI Circuits Digest of Technical Papers., 2 pgs.</td></tr> <tr><td>C9</td><td>HIROSHI NAKAMURA, JUNICHI MIYAMOTO, KENICHI IMAMIYA, YOSHIHISA IWATA, YOSHIHISA SUGIURA and HIDEKO OODAIRA, A Novel Sensing Scheme with On-Chip Page Copy for Flexible Voltage NAND Flash Memories, article, June 1996, 9 pgs., Vol. E79-C, No.6</td></tr> <tr><td>C10</td><td>TAKAAKI NOZAKI, TOSHIKAI TANAKA, YOSHIO KOIWA, EIJI KINOSHITA, TATSUO TSUCHIYA and YUTAKA HAYASHI, A 1-Mb EEPROM with MONOS Memory Cell for Semiconductor Disk Application, article, 1991, 5 pgs., Journal Of Solid-State Circuits, Vol.,26, No. 4.</td></tr> <tr><td>C11</td><td>KAI HWANG and FAYE A. BRIGGS, Computer Architecture and Parallel Processing, book, 1984, McGraw-Hill, Inc., 2 pgs., US</td></tr> <tr><td>C12</td><td>WALTER LAHTI and DEAN McCARRON, State of the Art: Magnetic VS. Optical Store Data in a Flash, article, 1990, 7 pgs., Vol. 15, No. 12, McGraw-Hill, Inc., US</td></tr> <tr><td>C13</td><td>RON WILSON, INTEGRATED CIRCUITS; 1-Mbit flash memories seek their role in system design, article, March 1, 1989, 2 pgs., No.6, Tulsa, OK</td></tr> <tr><td>C14</td><td>S. MEHROURA, J.H. YUAN, R.A. CEMEA, W.Y. CHIEN, D.C. GUTERMAN, G. SAMACHISA, R.D. NOMAN, M. MOFIDI, W.LEE, Y. FONG, A. MIHNEA, E. HANN, R.W. GREGOR, E.P. EBERHARDT, J.R. RADOSEVICH, K.R. STILES, R.A. KOHLER, C.W. LEUNG, and T.J. MULROONEY, Serial 9Mb EEPROM for Solid State Disk Applications symposium, 1992, 2 pgs., Mountain View, CA</td></tr> <tr><td>C15</td><td>STEVEN H. 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## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

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		First Named Inventor	Estakhri et al.
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PTO-1449

ATTY. DOCKET NO.  
LEXAR-11C1APPLICATION NO.  
09/264,340APPLICANT  
Estakhri et al.FILING DATE  
3/8/99GROUP  
2751

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A1	4,210,959	7/1/80	Wozniak	364	200	
A2	4,355,376	10/19/82	Gould	365	230	
A3	4,405,952	9/20/83	Slakmon	360	49	
A4	4,450,558	5/22/84	Bond et al.	371	10	
A5	4,456,971	6/26/84	Fukuda et al.	364	900	
A6	4,498,146	2/5/85	Martinez	364	900	
A7	4,525,838	6/25/85	Nozawa et al.	371	38	
A8	4,616,311	10/7/86	Sato	364	200	
A9	4,654,847	3/31/87	Dutton	371	10	
A10	4,710,871	12/1/87	Belknap et al.	364	200	
A11	4,746,998	5/24/88	Robinson et al.	360	72.1	
A12	4,748,320	5/31/88	Yorimoto et al.	235	492	
A13	4,757,474	7/12/88	Fukushi et al.	365	189	
A14	4,774,700	9/27/88	Satoh et al.	369	54	
A15	4,800,520	1/24/89	Iijima	364	900	
A16	4,896,262	1/23/90	Wayama et al.	364	200	
A17	4,914,529	4/3/90	Bonke	360	48	
A18	4,920,518	4/24/90	Nakamura et al.	365	228	
A19	4,924,331	5/8/90	Robinson et al.	360	72.1	
A20	4,953,122	8/28/90	Williams	364	900	
A21	5,070,474	12/3/91	Tuma et al.	385	500	
A22	5,168,465	12/1/92	Harari	257	320	
A23	5,198,380	3/30/93	Harari	437	43	
A24	5,200,959	4/6/93	Gross et al.	371	21.6	
A25	5,226,168	7/6/93	Kobayashi et al.	395	800	

Previous  
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3/8/99GROUP  
2751

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A26	5,268,318	12/7/93	Harari	437	43	
A27	5,268,870	12/7/93	Harari	365	218	
A28	5,270,979	12/14/93	Harari et al.	365	218	
A29	5,270,979	12/14/93	Harari et al.	365	218	
A30	5,293,560	3/8/94	Harari	365	185	
A31	5,297,148	3/22/94	Harari et al.	371	10.2	
A32	5,303,198	4/12/94	Adachi et al.	365	218	
A33	5,315,541	5/24/94	Harari et al.	365	63	
A34	5,337,275	8/9/94	Gamer	365	189.01	
A35	5,341,330	8/23/94	Wells et al.	365	185	
A36	5,341,339	8/23/94	Wells	365	218	
A37	5,353,256	10/4/94	Fandrich et al.	365	230.03	
A38	5,357,475	10/18/94	Hasbun et al.	365	218	
A39	5,369,615	11/29/94	Harari et al.	365	218	
A40	5,388,083	2/7/95	Assar et al.	365	218	
A41	5,396,468	3/7/95	Harari et al.	365	218	
A42	5,418,752	5/23/95	Harari et al.	365	218	
A43	5,422,842	6/6/95	Cerneia et al.	365	185	
A44	5,428,621	6/27/95	Mehrotra et al.	371	21.4	
A45	5,430,859	7/4/95	Norman et al.	395	425	
A46	5,430,859	7/4/95	Norman et al.	395	425	
A47	5,434,825	7/18/95	Harari	365	185	
A48	5,438,573	8/1/95	Mangan et al.	371	10.3	
A49	5,471,478	11/28/95	Mangan et al.	371	10.3	
A50	5,479,638	12/26/95	Assar et al.	395	430	

Previous (continued)

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## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A51	5,485,595	1/16/96	Assar et.al.	395	430	
A52	5,495,442	2/27/96	Cerneia et.al.	365	185.03	
A53	5,504,760	4/2/96	Harari et al.	371	40.1	
A54	5,508,971	4/16/96	Cerneia et.al.	365	185.23	
A55	5,524,230	6/4/96	Sakaue et al..	395	430	
A56	5,532,962	7/2/96	Auclair et al.	365	201	
A57	5,532,964	7/2/96	Cerneia et al.	365	189.09	
A58	5,534,456	7/9/96	Yuan et al.	437	43	
A59	5,535,328	7/9/96	Harari et al..	395	182.05	
A60	5,544,118	8/6/96	Harari	365	218	
A61	5,544,356	8/6/96	Robinson et al.	395	600	
A62	5,554,553	9/10/96	Harari	437	43	
A63	5,563,825	10/8/96	Cerneia et al.	365	185.18	
A64	5,566,314	10/15/96	DeMarco et.al.	395	430	
A65	5,568,438	10/22/96	Harari	365	218	
A66	5,583,812	12/10/96	Harari	365	185.33	
A67	5,592,420	1/7/97	Cerneia et al.	365	185.18	
A68	5,642,312	6/24/97	Harari	365	185.33	
A69	5,663,901	9/2/97	Wallace et al.	365	52	
A70	5,693,570	12/2/97	Cerneia et al.	437	205	
A71	5,712,819	1/27/98	Harari	365	185.29	
A72	5,719,808	2/17/98	Harari et al.	365	185.33	
A73	5,778,418	7/7/98	Auclair et al.	711	101	

Previously Considered

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P A T E N T S T R A D E M A R K S O C I E T Y  
**INFORMATION DISCLOSURE STATEMENT**

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2751**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS	Translation	
							Yes	No
B1	4-332999 (A)	19.11.1992	JP	Masakazu Namekawa	G11C	29/00	X	
B2	58-215794 (A)	15.12.1983	JP	Noriyuki Tanaka	G11C	17/00	X	
B3	58-215795 (A)	15.12.1983	JP	Noriyuki Tanaka	G11C	17/00	X	
B4	59-45695 (A)	7.9.1982	JP	Yuuichi Furukawa	G11C	17/00	X	
B5	59-162695 (A)	13.9.1984	JP	Toshio Niromiya(1)	G11C	17/00	X	
B6	60-212900	25.10.1985	JP	Tsuneaki Higashi	G11C	29/00	X	
B7	61-96598 (A)	15.5.1986	JP	Yutaka Haniyu	G11C	17/00	X	
B8	62-283496 (A)	09.12.1987	JP	Shinichi Nakada	G11C	17/00	X	
B9	62-283497 (A)	09.12.1987	JP	Shinichi Nakada	G11C	17/00	X	
B10	63-183700 (A)	29.7.1988	JP	Kazuo Ishikawa	G11C	17/00	X	
B11	0 557 723	08.1.1987	AU	Kevin John Burke	G11C	5/00	X	
B12	0 220 718 A2	06.5.1987	EP	Yorimoto et al.	G06F	15/40	X	
B13	0 243 503 A1	04.11.1987	EP	Fukushima et al.	G11B	20/10	X	
B14	0 424 191 A2	24.04.1991	EP	Stephen Gross	G06F	11/00	X	
B15	0 489 204 A1	10.06.1992	EP	Kevin Lloyd-Jones	G11C	16/06	X	
B16	0 522 780 A2	13.01.1993	EP	Sakae et al.	G06F	3/06	X	
B17	0 544 252 A2	02.06.1993	EP	Matsui et al.	G11C	16/06	X	
B18	0 686 976 A2	13.12.1995	EP	Matsui et.al.	G11C	16/06	X	
B19	93 01908	27.08.1993	FR	Hiroyuki et al.	G06F	12/02	X	
B20	84/00628	16.02.1984	WO	Maria Martinez	G06F	11/20, 13/00	X	

<b>INFORMATION DISCLOSURE CITATION</b> <small>SEP 21 2001 PTO-1449</small>		ATTY. DOCKET NO. LEXAR-11C1	APPLICATION NO. 09/264,340
		APPLICANT Estakhri et al.	
		FILING DATE 3/8/99	GROUP 2751
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
<p>C1 Book - Computer Architecture and Parallel Processing; Kai Hwang &amp; Faye A. Briggs, McGraw-Hill Book Co., © 1984, Page 64.</p> <p>C2 Magazine - "State of the Art: Magnetic VS. Optical Store Data in a Flash", by Walter Lahti and Dean McCarron, Byte magazine dated 11/1/90 , 311, Vol. 15, No. 12.</p> <p>C3 Magazine - Technology-Updates, Integrated Circuits, "1-Mbit flash memories seek their role in system design", Ron Wilson, Senior Editor, Computer Design magazine 28 (1989) March 1, No.5, Tulsa OK, US, pages 30 and 32..</p> <p>C4 1992 Symposium of VLSI Circuits Digest of Technical Papers, "EEPROM for Solid State Disk Applications", S. Mehoudi et.al., SunDisk Corporation, Santa Clara, CA. R. W. Gregor et al., AT&amp;T Bell Laboratories, Allentown, PA. Pages 24 and 25.</p> <p>[15 blank lines]</p>			
EXAMINER		DATE CONSIDERED	

Previously Considered

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